Features

- Ultra-low Q_G For High Efficiency
- Logic Level
- Light Weight 0.068 grams
- No Wire Bond for Higher Reliability and Low Inductance
- Total Dose
 - Rated to 1000 krad
- Single Event
 - SEE immunity for LET of 85 MeV/(mg/cm²⁾ with V_{DS} up to 100% of rated Breakdown
- Low Dose Rate at 100 mRad/sec
 - Maintains Pre-Rad specification
- Neutron
 - Maintains Pre-Rad specification for up to 3 x 10¹⁵ Neutrons/cm²

Application

- Commercial Satellite EPS & Avionics
- Deep Space Probes
- High Speed Rad Hard DC-DC Conversion
- Rad Hard Motor Controllers
- Nuclear Facilities

Thermal Characteristics

Symbol	Parameter-Conditions	Value	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient (Note 3)	200	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	TBD	C/ VV



EPC7014UB

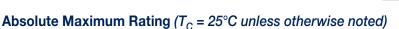
Rad Hard e-GaN[®] 60 V, 1 A, 580 m Ω Surface Mount (UB)

Description

EPC Space Rad Hard eGaN[®] power switching HEMTs have been specifically designed for critical applications in the high reliability or commercial satellite space environments. These devices have exceptionally high electron mobility and a low temperature coefficient resulting in very low $R_{DS(on)}$ values. The lateral structure of the die provides for very low gate charge (Q_G) and extremely fast switching times. These features enable faster power supply switching frequencies resulting in higher power densities, higher efficiencies and more compact packaging.

I/O Pin Assignment (Bottom View)

Pin	Symbol	Description	1
1	G	Gate	2
2	D	Drain	and the second second
3	S	Source	
4	L	Lid Pad Connection	



Symbol	Parameter-Conditions	Value	Units	
M	Drain to Source Voltage (Note 1)	60	V	
V _{DS}	Drain-to-Source Voltage (up to 10,000 5 ms pulses at 150°C)	72	V	
I _D	Continuous Drain Current ID @ $V_{GS} = 5 \text{ V}, T_C = 25^{\circ}\text{C}, R_{\theta JA} < 40 ^{\circ}\text{C/W}$ 1			
I _{DM}	Single-Pulse Drain Current $t_{pulse} \le 80 \ \mu s$	4	A	
V _{GS}	Gate to Source Voltage (Note 2)	+7 / -4	V	
T_{J},T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	*0	
T _{sol}	Package Mounting Surface Temperature 260		°C	
ESD	ESD Class	Δ1Α		

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Electrical Characteristics (*T_C* = 25°C unless otherwise noted. Typical (TYP) values are for reference only.)

Parameter	Symbol	Test Cond	ditions	MIN	ΤΥΡ	MAX	Units
Drain to Source Voltage	B _{VDSS}	$V_{GS} = 0 V, I_{D} = 0.1 mA$		60	-	-	V
Drain to Source Leakage	1	V _{DS} = 60 V	$T_{\rm C} = 25^{\circ}{\rm C}$	-	10	100	
Drain to Source Leakage	DSS	$V_{GS} = 0 V$	$T_{\rm C} = 125^{\circ}{\rm C}$	-		1000	
Gate to Source Forward Leakage	I _{GSS}	$V_{GS} = 5 V$	$T_{\rm C} = 25^{\circ}{\rm C}$	-	0.01	500	μA
Gate to Source Reverse Leakage	I _{GSS}	V _{GS} = -3 V	$T_{\rm C} = 25^{\circ}{\rm C}$		-50	-100	
Gate to Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 0.14 \text{ mA}$	$T_{\rm C} = 25^{\circ}{\rm C}$	0.8	1	2.5	V
Gate to Source Threshold Voltage Temperature Coefficient	$\Delta V_{GS(th)}/\Delta T$	$V_{DS} = V_{GS}, I_{D} = 0.14 \text{ mA}$	-55°C < T _A < 150°C	-	-	-	mV/°C
Drain to Source Resistance (Note 4)	R _{DS(on)}	$I_{\rm D} = 1 \text{ A}, V_{\rm GS} = 5 \text{ V}$	$T_{\rm C} = 25^{\circ}{\rm C}$	-	340	580	mΩ
Source to Drain Forward Voltage (Note 5)	V _{SD}	$I_{\rm S} = 0.5 \text{ A}, V_{\rm G} = 0 \text{ V}$	$T_{\rm C} = 25^{\circ}{\rm C}$		2.5		V

Dynamic Characteristics ($T_c = 25^{\circ}$ C unless otherwise noted. Typical (TYP) values are for reference only.)

Parameter	Symbol	Test Conditions	MIN	ΤΥΡ	MAX	Units
Input Capacitance	C _{ISS}			16	22	
Output Capacitance	C _{OSS}	$V_{DS}=30 \text{ V}, V_{GS}=0 \text{ V}$		0.1		
Reverse transfer Capacitance	C _{RSS}			17	26	pF
Effective Output Capacitance, Energy Related (Note 2)	C _{OSS(ER)}	<u> </u>		21		p.
Effective Output Capacitance, Time Related (Note 3)	C _{OSS(TR)}	$V_{DS} = 0$ to 30 V, $V_{GS} = 0$ V		26		
Gate Resistance	R _G			12.6		Ω
Total Gate Charge (Note 7)	Q _G	V_{DS} = 0 to 30 V, V_{GS} = 0 V		142	184	
Gate to Source Charge (Note 7)	Q _{GS}			43		
Gate to Drain Charge (Note 7)	Q _{GD}	$V_{DS} = 30$ V, $I_{D} = 0.5$ A		25		-0
Gate to Source Charge (Note 7)	Q _{G(TH)}			35		рС
Output Charge (Note 8)	Q _{OSS}	$V_{DS} = 30 \text{ V}, \ V_{GS} = 0 \text{ V},$		764	1146	
Source to Drain Recovery Charge	Q _{RR}			0		

Defined by design. Not subject to production test. Note 2: $C_{OSS(ER)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 50% BV_{DSS}. Note 3: $C_{OSS(TR)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 50% BV_{DSS}.

Radiation Characteristics

EPC Space eGaN[®] HEMTs are tested according to MIL-STD-750 Method 1019 for total ionizing dose validation. Every manufacturing lot is tested for total ionizing dose of Gamma radiation with an in-situ bias for (i) $V_{GS} = 5 V$, (ii) $V_{DS} = V_{GS} = 0 V$ and (iii) $V_{DS} = 80\% B_{VDSS}$.

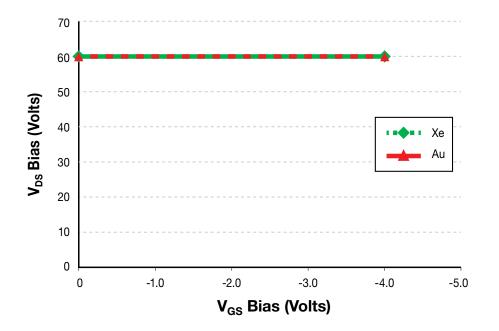
Parameter	Symbol	Test Conditions	MIN	ТҮР	MAX	Units
Drain to Source Voltage	B _{VDSS}	V _{GS} = 0 V, I _D = 0.1 mA	60	-	-	N
Gate to Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 2 \text{ mA}$	0.8	1.0	2.5	V
Drain to Source Leakage	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	-	0.01	100	
Gate to Source Forward Leakage	I _{GSS}	V _{GS} = 5 V	-	0.5	500	μA
Gate to Source Reverse Leakage	I _{GSS}	$V_{GS} = -4 V$	-	-0.5	-100	
Drain to Source Resistance (Note 4)	R _{DS(on)}	I _D = 1 A, V _{GS} = 5 V	-	340	580	mΩ

Electrical Characteristics up to 1000 krads ($T_c = 25^{\circ}C$ unless otherwise noted. Typical (TYP) values are for reference only.)

Typical Single Event Effect Safe Operating Area

Note : All Single Event Effect testing is performed on the K-500 Cyclotron at Texas A&M University

Test		Envir		V _{DS} Vol	tage (V)	
	lon	LET MeV/mg/cm ²	Range µm	Energy MeV	$V_{GS} = 0 V$	$V_{GS} = -4V$
See SOA	Xe	50	131	1653	60	60
	Au	83.7	130	2482	60	60



Typical Single Event Effect Safe Operating Area

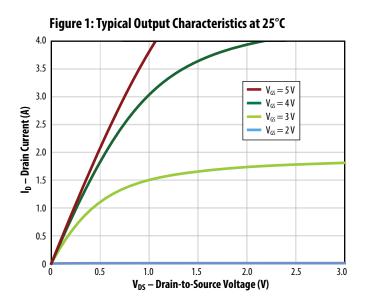
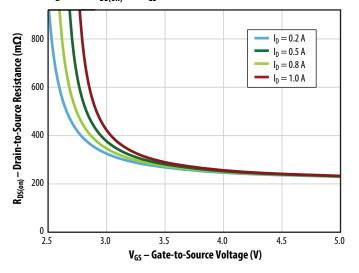
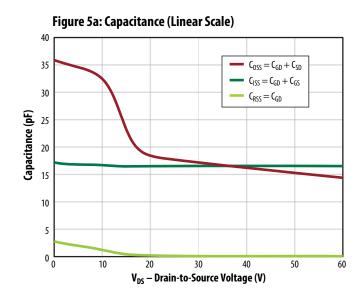


Figure 3: $R_{DS(on)}$ vs. V_{GS} for Various Drain Currents





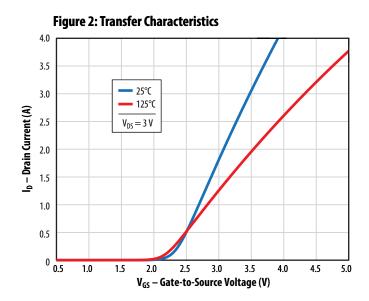
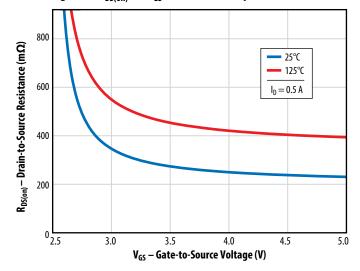
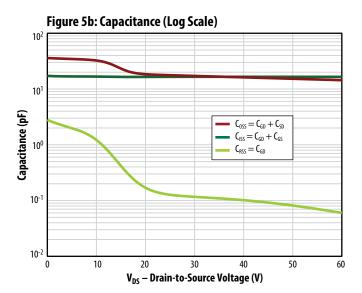
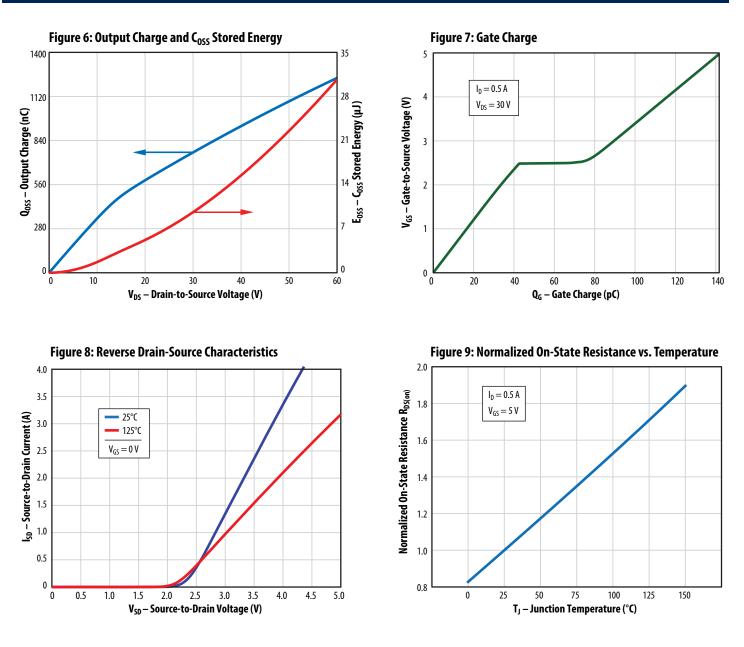


Figure 4: R_{DS(on)} vs. V_{GS} for Various Temperatures

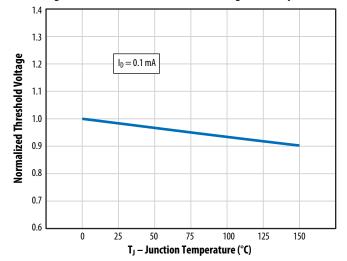




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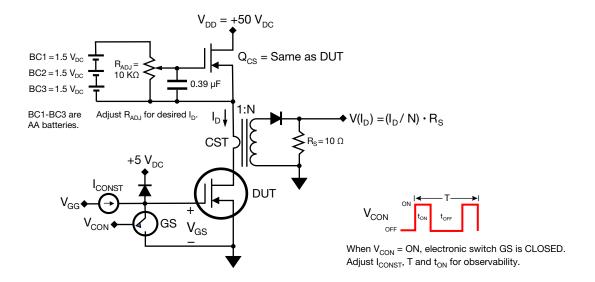


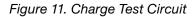




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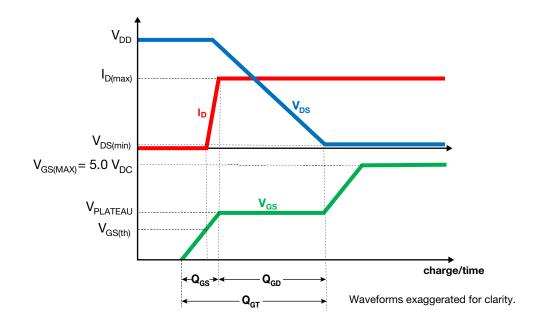
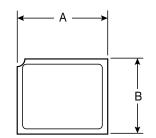
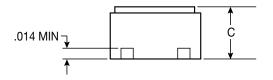
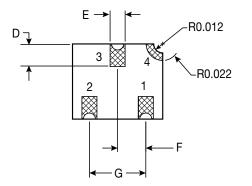


Figure 12. Typical Gate Charge Test Waveform

Package Outline and Dimensions



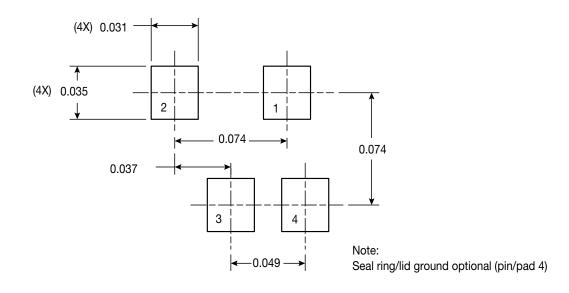




Symbol	Inches		Millim	Note	
Cynisor	MIN	MAX	MIN	MAX	
Α	0.115	0.128	2.921	3.251	
В	0.095	0.108	2.413	2.743	
С	0.050	0.069	1.270	1.752	
D	0.022	0.024	0.559	0.610	
Е	0.0152	0.024	0.381	0.610	
F	0.035	0.039	0.889	0.991	
G	0.071	0.079	1.803	2.006	

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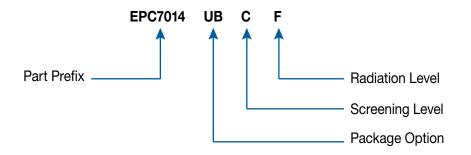
UB Footprint for Printed Circuit Board Design



Notes

- Note 1. NEVER exceed the absolute maximum V_{DS} of the device otherwise permanent damage/destruction may result.
- Note 2. NEVER exceed the absolute maximum V_{GS} of the device otherwise permanent damage/destruction may result. We recommend use at no greater than +5 V as the HEMT is fully conducting at this point.
- Note 3. R_{0JA} measured with LCC3 package mounted to double-sided PCB, 0.063" thickness with 1.0 square inches of copper area on the top (mounting side) and a flood etch (3 square inches) on the bottom side.
- Note 4. Measured using four wire (Kelvin) sensing and pulse measurement techniques. Measurement pulse width is 80 µs and duty cycle is 1%, maximum.
- Note 5. Operation of the device in the third quadrant region is not recommended.
- Note 6. $C_{ISS} = C_{GS} + C_{GD}$ with C_{DS} shorted. $C_{OSS} = C_{DS} + C_{GD}$. $C_{RSS} = C_{GD}$.
- Note 7. The gate charge parameters are measured using the circuit shown in Figure 11. Qs and associated components BT1, P1 and C1 form a high speed current source that serves as the test load for the DUT. A constant gate current (I_{const}) of 1.5-3 mA is provided to the Gate of the DUT during the time that the ground switch (G_S) is OFF (t_{off}). The DUT is switched ON and OFF using ground-sensed switch GS. The gate current is adjusted to yield the desired charge per unit time (I_{const} · time per division) on the measuring oscilloscope. The GS pulse drive ON time (t_{on}) is adjusted for the desired observability of the gate-source voltage (V_{GS}) waveform. The maximum duty cycle of the ground switch (t_{off}/t_{on}) should be set to 1% maximum. Please note that all gate-related signals are referenced to the "Source Sense" pin on the package. At all times during the measurement, the maximum gate-source voltage is clamped to 5 V_{DC}.
- Note 8. Guaranteed by design/device construction. Not tested.

EPC Space Part Number Information



Ordering Information Availability

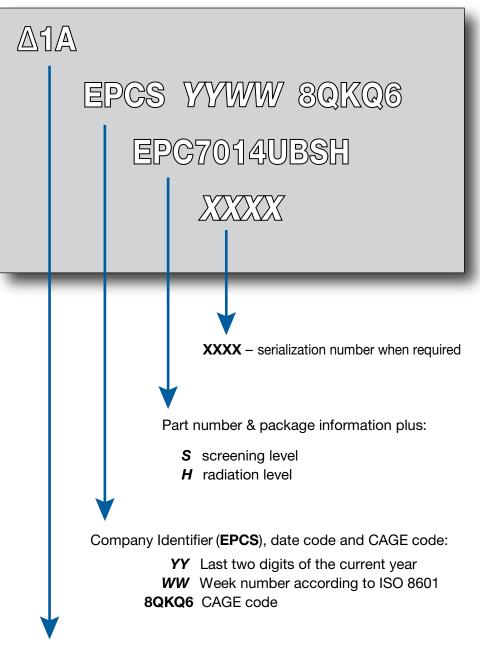
Screening Options	Rad Assurance Options
1 character	1 character
C = Developmental Unit V = Lite Screened S = Space Level ¹	R = 100 krad F = 300 krad G = 500 krad H = 1000 krad Z = 1000 krad

Part Number	Screening Level	Shipping
EPC7014UBC	Engineering Samples	Waffle trave
EPC7014UBSH	Space Level	Waffle trays

¹ Screening and qualification consistent to an equivalent MIL-PRF-19500 specification.

C version FSMD-A units are intended for engineering development purposes only and NOT supplied with radiation performance guarantees nor supplemental data packages

EPC Space Product Marking Information



ESD rating of the device located directly over the Gate pin

Data Package Order Detail Consistent to MIL-PRF-19500 general specification

SPACE Screen

1. EPC7014UB*S - STANDARD DATA PACKAGE

- A. Certificate of Compliance
- **B. Serialization Records**
- C. Preconditioning Attributes Data Sheet
 - HTRB Hi Temp Gate Stress Post Reverse Bias Data and Delta Data
 - HTRB Hi Temp Drain Stress Post Reverse Bias Delta Data
- D. Group A Attributes Data Sheet
- E. Group B Attributes Data Sheet
- F. Group D Attributes Data Sheet

2. EPC7014UB*S - OPTIONAL DATA PACKAGE

- A. Certificate of Compliance
- **B. Serialization Records**
- C. Assembly Flow Chart
- D. SEM Photos and Report
- E. Preconditioning Attributes Data Sheet
 - HTRB Hi Temp Gate Stress Post Reverse Bias Data and Delta Data
 - HTRB Hi Temp Drain Stress Post Reverse Bias Delta Data
 - X-Ray and X-Ray Report
- F. Group A Attributes Data Sheet
 - Subgroups A1, A2, A3 and A7 Data
- G. Group B Attributes Data Sheet
 - Subgroups B1, B2, B3, B4, B5 and B6 Data
- H. Group C Attributes Data Sheet
 - Subgroups C1, C2, C3, C4, C6 and C7 Data
- I. Group D Attributes Data Sheet
 - Pre and Post Radiation Data

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Revisions

Datasheet Revision	Product Status
REV Q1	Characterization and Qualification
Preliminary	Production Released

Information subject to change without notice. Revised July, 2021



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